



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/663,644      | 09/17/2003  | Isamu Kaneyasu       | 031159              | 3814             |

38834 7590 04/04/2006

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP  
1250 CONNECTICUT AVENUE, NW  
SUITE 700  
WASHINGTON, DC 20036

EXAMINER

EISEN, ALEXANDER

ART UNIT PAPER NUMBER

2629

DATE MAILED: 04/04/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/663,644

Applicant(s)

KANEYASU, ISAMU

Examiner

Alexander Eisen

Art Unit

2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 17 September 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-14 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 10-14 is/are allowed.
- 6) ☒ Claim(s) 1,3-6 and 9 is/are rejected.
- 7) ☒ Claim(s) 2,7 and 8 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 17 September 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date <u>9/17/03</u> . | 6) <input type="checkbox"/> Other: _____  |

**DETAILED ACTION**

***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1, 6, 9 are rejected under 35 U.S.C. 102(b) as being anticipated by Page, US 5,107,189.

In regard to claim 1 Page discloses method for controlling a video signal circuit (FIGS. 3-4) including a preamplifier circuit 37 for amplifying a video signal and adding a bias voltage (from gm amplifier 35) to the same and an output circuit 11 for amplifying power of the signal that is delivered from the preamplifier circuit, wherein a level of the bias voltage that is applied by the preamplifier circuit is changed in accordance with contents of the video signal (see feedback from the output circuit 11 through a resistor 19, gm 35 and a capacitor 38 providing a bias depending on the video signal; col. 6, lines 30-44).

With respect to claim 6, Page discloses a display device (CRT 10 in FIG. 3) for displaying images on a screen in accordance with a video signal, comprising a preamplifier circuit 25 for amplifying a supplied video signal (VIDEO IN 22) and adding a bias voltage (from CLAMP CAP 47) to the same; an output circuit 11 for amplifying power of the signal that is delivered from the preamplifier circuit; and a variable bias circuit (gm amplifier 44, capacitor 47 and potentiometer 46) for changing a level of the bias voltage that is added in the preamplifier

Art Unit: 2629

circuit in accordance with an adjustment signal (BRIGHTNESS CONTROL 46). See also column 4, lines 41-61).

As pertaining to claim 9, Page further teaches an operation portion (mixer 43 in FIG. 3) for adjusting the level of the bias voltage, wherein the variable bias circuit ( $g_m$  amplifier 44) uses a signal from the operation portion (mixer 43) as the adjustment signal for changing the level of the bias voltage.

3. Claims 3-5 are rejected under 35 U.S.C. 102(b) as being anticipated by Ferreira, US 4,701,797.

With respect to claim 3 Ferreira discloses a video signal circuit (FIGS. 1-2) comprising a preamplifier circuit TR1 for amplifying a supplied video signal and adding a bias voltage (col. 6, line 40-47) to the same; an output circuit TR12 for amplifying power of the signal that is delivered from the preamplifier circuit Through resistor R44); and a variable bias circuit (diodes D1-D4 and potentiometer P2) for changing a level of the bias voltage that is added in the preamplifier circuit.

As pertaining to claim 4, the output circuit TR12 amplifies power of the signal that is delivered from the preamplifier circuit TR1 and inverts the same (since the power transistor is a common emitter circuit the output amplified video signal from the collector of the transistor TR12 is inverted).

As pertaining to claim 5, changing the bias of the transistor TR1 by the potentiometer P2 would inherently effect the gain of the pre-amplifier based on the TR1 since it would move the quiescence point of the pre-amplifier along I/U characteristics curves.

*Allowable Subject Matter*

4. Claims 10-14 are allowed.

The following is an examiner's statement of reasons for allowance: none of the references have been found that teach or fairly suggest a modification of, or a combination with the cited prior art so as to arrive to the combination of the independent claims 10 and 13; , namely, a computer system comprising a computer body and a display device for displaying images on a screen in accordance with a video signal generated in the computer body, comprising: a video signal circuit including a preamplifier circuit for amplifying the video signal and adding a bias voltage to the same, an output circuit for amplifying power of the signal that is delivered from the preamplifier circuit, a reception circuit for receiving a video type signal related to contents of the video signal, and a variable bias circuit for changing a level of the bias voltage that is added in the preamplifier circuit in accordance with the video type signal; a video detection portion for generating the video type signal; and a transmission portion for transmitting the generated video type signal to the reception circuit, or a computer program that is used for a computer that can display images on a screen of a display device in accordance with a generated video signal, the program makes the computer perform the steps of: generating a video type signal for discriminating the video signal that is either a video signal related to a still image or a video signal related to a moving image; and transmitting the generated video type signal to the display device.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

Art Unit: 2629

fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

5. Claims 2, 7, 8 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

6. The following is a statement of reasons for the indication of allowable subject matter: none of the references teaches or suggest a modification of, or a combination with the cited prior art so as to arrive to a combination of the limitations of:

- Claim 2; a method for controlling a video signal circuit including a preamplifier circuit for amplifying a video signal and adding a bias voltage to the same and an output circuit for amplifying power of the signal that is delivered from the preamplifier circuit, wherein a level of the bias voltage that is applied by the preamplifier circuit is changed in accordance with contents of the video signal, wherein the level of the bias voltage is changed in accordance with the video signal that is either a video signal related to a still image or a video signal related to a moving image.
- Claim 7; a display device for displaying images on a screen in accordance with a video signal, comprising a preamplifier circuit for amplifying a supplied video signal and adding a bias voltage to the same; an output circuit for amplifying power of the signal that is delivered from the preamplifier circuit; and a variable bias circuit for changing a level of the bias voltage that is added in the preamplifier circuit in accordance with an adjustment signal, wherein the variable bias circuit increases the bias voltage if the video

Art Unit: 2629

signal relates to a still image and decreases the bias voltage if the video signal relates to a moving image.

- Claim 8; a display device for displaying images on a screen in accordance with a video signal, comprising a preamplifier circuit for amplifying a supplied video signal and adding a bias voltage to the same; an output circuit for amplifying power of the signal that is delivered from the preamplifier circuit; and a variable bias circuit for changing a level of the bias voltage that is added in the preamplifier circuit in accordance with an adjustment signal, and further comprising a reception circuit for receiving a video type signal for discriminating whether the video signal relates to a still image or to a moving image, wherein the variable bias circuit uses the video type signal as the adjustment signal for changing the level of the bias voltage.

### *Conclusion*

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

**Morrish, US 6,208,094 B1.**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alexander Eisen whose telephone number is (571) 272-7687. The examiner can normally be reached on M-F (9:00-5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard A. Hjerpe can be reached on (571) 272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2629

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to read 'Alexander Eisen', is positioned to the left of the printed name.

Alexander Eisen  
Primary Examiner  
Art Unit 2629

21 March 2006